

APPLICANT(S): Goren et al
SERIAL NO.: 10/091,934
FILED: March 6, 2002
Page 2

AMENDMENTS TO THE CLAIMS/CLAIM LISTING

Kindly amend the claims as follows:

1 - 41 (Cancelled)

42. (Cancelled)

43. (Previously Presented) An integrated circuit design library comprising:

a set of transmission line topologies for critical interconnect lines of an integrated circuit capable of carrying analog and mixed signals, which topologies comprise return paths therein; and

a set of parametrized, equivalent RLC ladder networks, one per topology.

44. (Previously Presented) The library according to claim 43 and also comprising means for performing both frequency and time domain analysis for each transmission line placed into an integrated circuit design.

45. (Previously Presented) The library according to claim 43 and wherein at least an inductance parameter of each said transmission line topology is a function of frequency

46. (Cancelled)

47. (Previously Presented) The library according to claim 45 and wherein said frequency ranges from DC to a transistor cut-off frequency.

48. (Cancelled).